

# Scientech Corp (3583:TT)

M.T. Hsu 2025/09/03

## Safe Harbor Statement



 This Presentation contains certain forward-looking statements that are based on current expectations and are subject to known and unknown risks and uncertainties that could cause actual results to differ materially from those expressed or implied by such statements.

 Except as required by law, we undertake no obligation to update any forward-looking statements, whether as a result of new information, future events or otherwise.

# Agenda







### **ABOUT US**

Overview | Milestones | Location | Certificate | Competence









#### **Product and Technology**

- **Equipment Design and Manufacturing**
- Wafer Reclaim
- Representative



#### **Industries We Serve**

- Semiconductor (Front-End and Advanced Packaging)
- Compound Semiconductor
- \_ED / Mini LED / Micro LED
- Flat Panel Display (TFT-LCD, AMOLED, Touch Panel)



**Established:** 1979 Taipei, Taiwan Capital: **US\$ 27 Million** TWSE:3583

**Employees:** >1,118 (Worldwide)

## **Staffs**



#### Over 1,118 Staffs

### **Representative**

- 140 in Taiwan
- 127 in China
- 70% in Service and Process Support

### Equipment Design and Maf'g

- 442 in Total
- 160+ in R&D, Process and Design

#### ക്ഷ Wafer Reclaim

323 in Total

### ക്ക Administration

86 in Total









1995 Subsidiary in CA, USA



2001 Subsidiary in Shanghai, China



2002 Singapore



2003 **Equipment Manufacture** Department Established









2021 Subsidiary in Villach, Austria





Over 500 Equipment Sales Achievement

2013

Publicly Listed in Taiwan Stock Market 2007

**New Factory** in Hukou Opened 2006

Wafer Reclaim **Business Launched**  2004

New Hsinchu Office Opened













#### **Taipei Headquarter**

#### **HuKou Factory**

- Equipment Design and Manufacturing
  - Equipped With Class 10/1,000/10,000 Clean Room
- Wafer Reclaim



**Kaohsiung Office** 

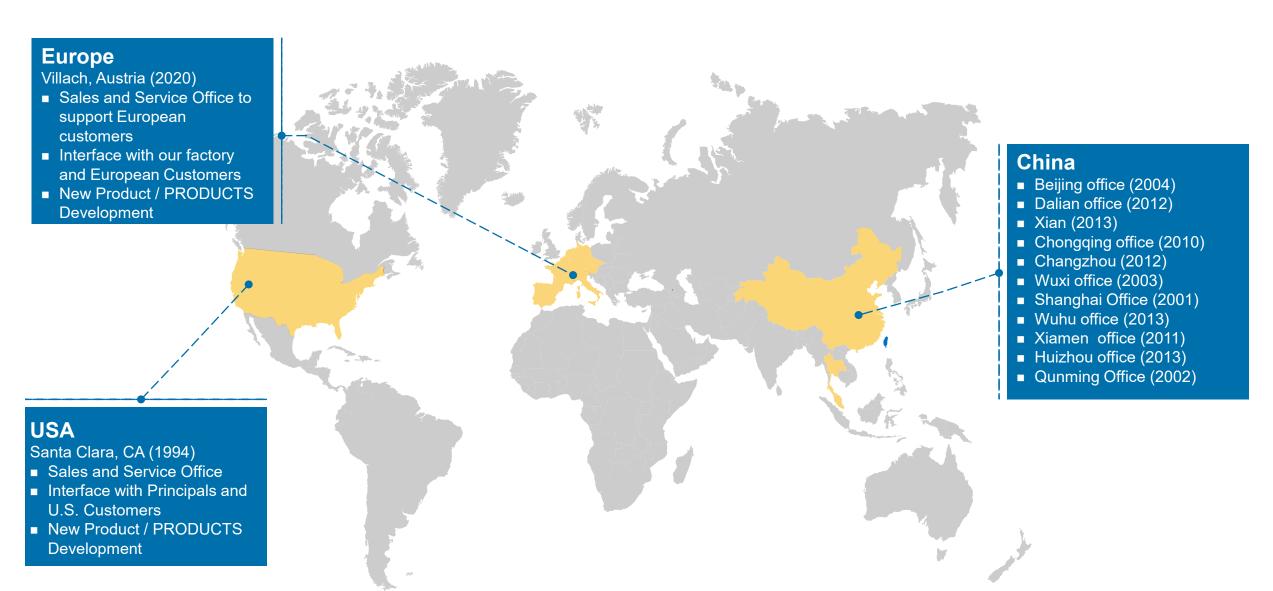
#### **HsinChu Office**



**Taichung Office** 

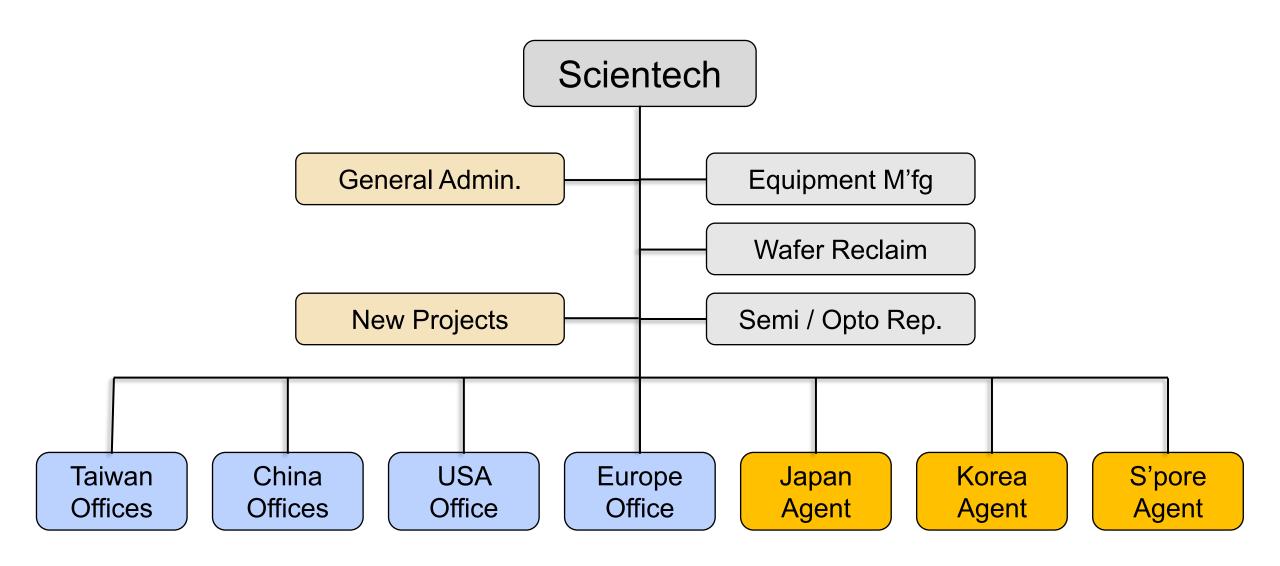
**Tainan Office** 





# Organization







### **PRODUCTS**

Wafer Reclaim | Equipment Manufacturing and Design | Representative

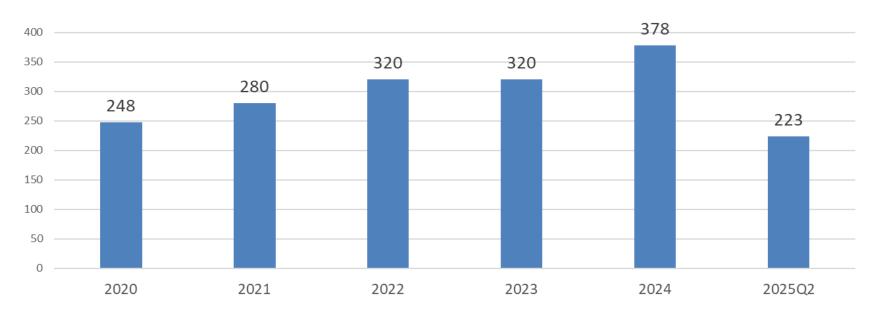


# R&D Expenses



Units NT \$ M	2020	2021	2022	2023	2024	2025Q2
R&D Expenses	248	280	320	341	378	223
Expenses as % of Revenue	6.9%	6.0%	5.7%	4.9%	3.9%	3.9%
Expenses as % of Manufacturing	16.5%	14.9%	15.3%	15.7%	11.3%	9.9%

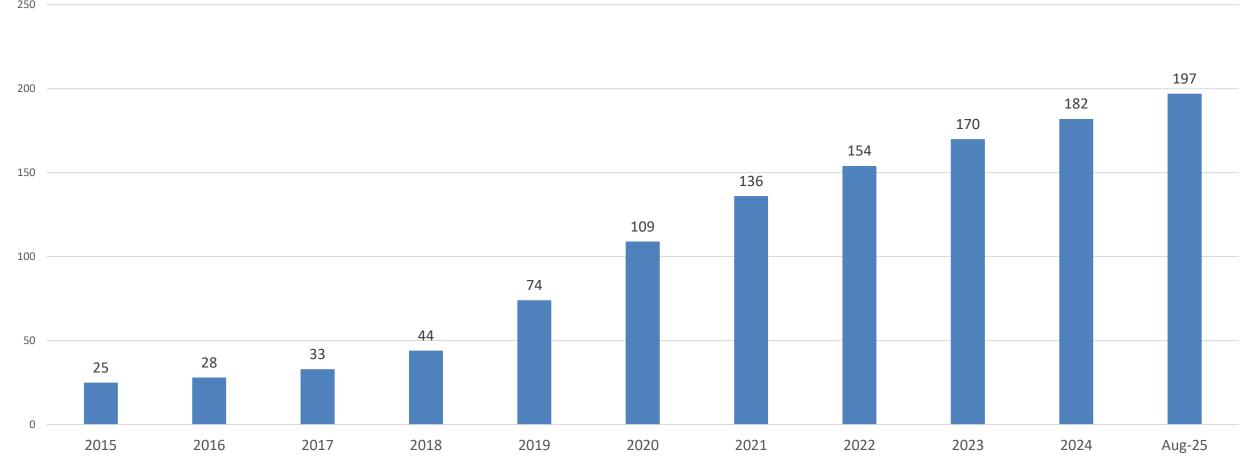
**R&D** Expenses



## **R&D Patents Number**



 The cumulative number of patents on file is 197, and 49 applications are pending.



## **Products**



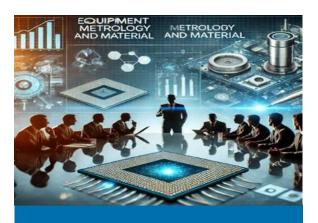


- Advanced Package
- **FEOL**
- Mask
- Compound
- etc...



#### Wafer Reclaim

12" Si Wafer



#### Representative

- Equipment/Metrology
- Sub-System
- Material
- Repair and Refurbish



#### Advanced Clean Technology

- 19nm Particle
- Low Trace Metal (<1E9)</li>

**Advanced Defect Inspection (SP1/SP2/SP5/SP7)** 





Grinding



- Capacity: 160K / Month
- Cu and Non-Cu Process





 Additional 50K / Month by end of 2025

Polishing

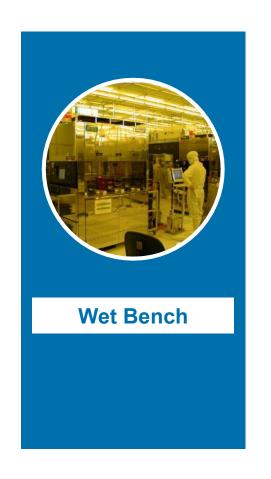
**Complete Polishing Process** 

- Single/Double Side Polish
- Final Haze Polish

Super Flatness (GBIR < 0.5mm)

## **Equipment Design & Manufacturing**













#### Wet Process – Bench



- Semiconductor: FEOL, BEOL, Compound
- Advanced Packaging/Bumping...



- Cassette type or Cassette-less type
- Adopted by big international companies
- Leading-edge technologies

- Etch: Metal/Oxide/Nitride remove
- Stripper: PR/PI strip, Polymer remove
- Clean: Pre/Post/Flux clean
- Electro-less Plating: Zn/Ni/Pd/Au





### Wet Process – Single Wafer

### **Application**

- Semiconductor: BEOL, FEOL, Compound
- Advanced Packaging/Bumping

### **Features**

- Soak/Immersion + Single
- Adopted by big international companies
- Leading-edge technologies

- UBM / Metal Etch
- Flux Clean (Hot DIW)
- PR Strip
- Wafer Clean (APC, Soft spray)
- Final Clean/Mask Clean/Frame Clean





### **Temporary Bonding De-Bonding**



- IGBT & SiC Power Device
- Advanced Packaging for Semiconductor



- User-friendly graphical user interface
- Adopted by big international companies
- Leading-edge technologies

- Temporary Bonding
- Temporary De-bonding
- Release Layer Coating
- Carrier (Glass) Recycling







### **Moisture Baking**



- Semiconductor: BEOL
- Advanced Packaging for Semiconductor

### **Features**

- Availability for horizontal or vertical process
- High precision temperature control
- Weight handling capacity up to 23kg per lot

- Pre-underfill baking
- Pre-mold baking
- DFR Hard Bake



# **Trading (Agent/Distributor)**



























**Exclusivity** 































#### **Representative Product Lines**

**PRODUCTS ABOUT US CONTACT US** 













**ESOL** 































#### **Representative Product Lines**

**PRODUCTS ABOUT US CONTACT US** 

















































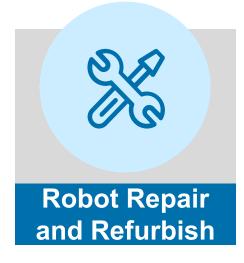


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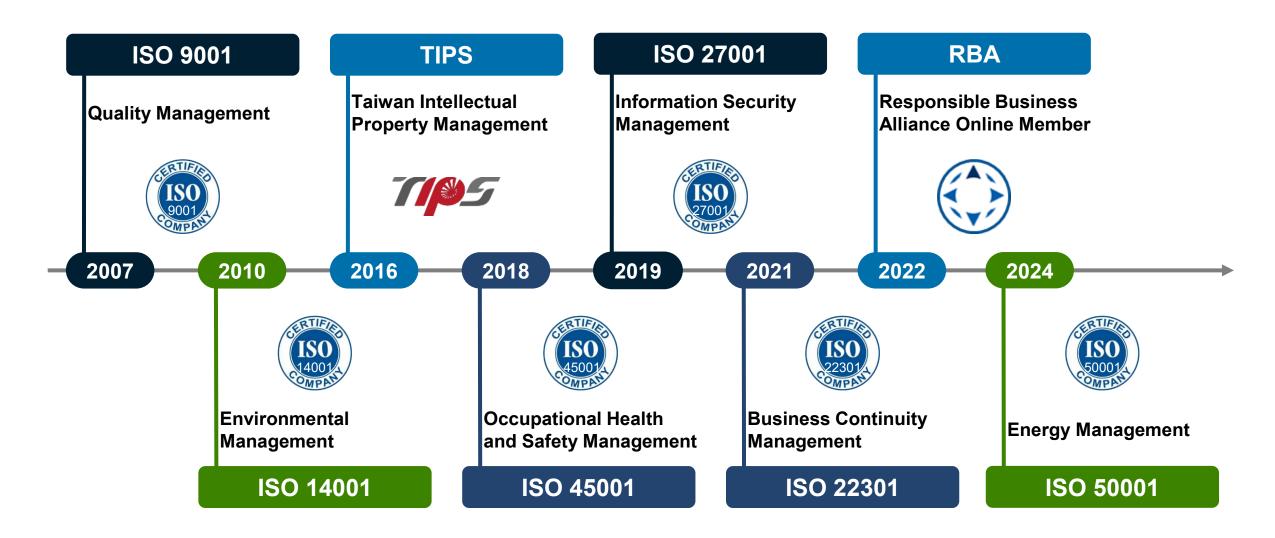


## **Corporate Governance**



## Certificates





### **ESG**



### Sustainability Report Officially Released in 2024/08



## **Exhibition**











## **Exhibition**









## **Financial Results**



# **Income Statement**

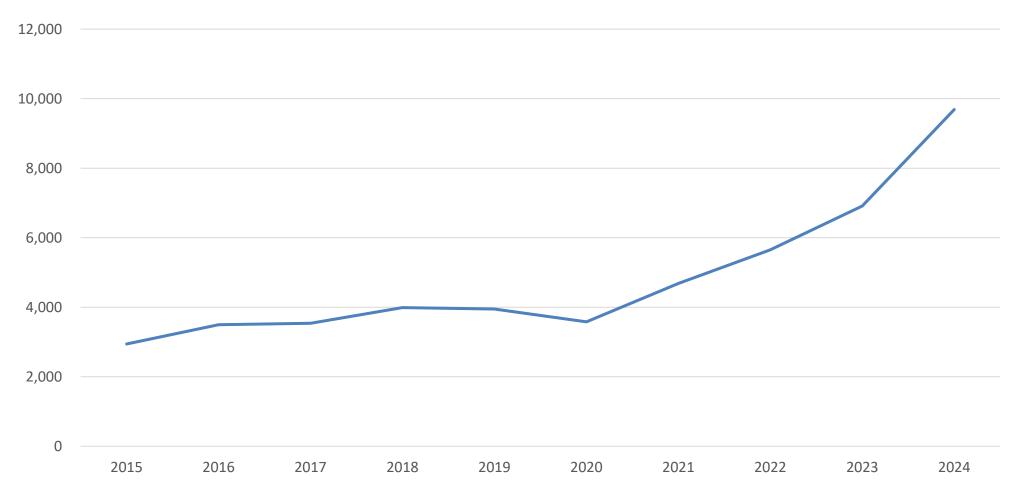


Units : NT \$ M	2020	2021	2022	2023	2024	2025Q2
Revenues	3,580	4,684	5,650	6,911	9,688	5,726
Gross Profit	1,456	1,667	2,084	2,201	2,906	1,858
Operating Expenses	991	1,112	1,374	1,483	1,790	1,038
Operating Income	465	555	710	718	1116	819
Income Before Tax	389	524	736	860	1277	650
Net Income	305	420	568	650	927	486
EPS	3.80	5.23	7.08	<u>8.10</u>	11.54	6.05
Gross Margin	41%	36%	37%	32%	30%	32%
Operating Margin	13%	12%	13%	10%	12%	14%
Income Before Tax Margin	11%	11%	13%	12%	13%	11%

## Revenue



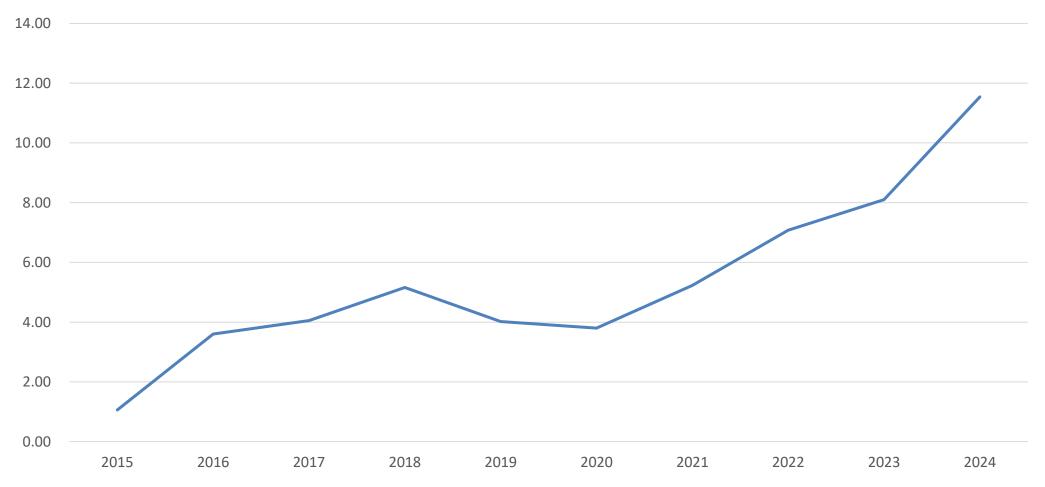
Units NT \$ M	2015	2016	2017	2018	2019	2020	2021	2022	2023	2024	2025Q2
Revenue	2,942	3,495	3,539	3,988	3,949	3,580	4,684	5,650	6,911	9,688	5,726



## **EPS**



	2015	2016	2017	2018	2019	2020	2021	2022	2023	2024	2025Q2
EPS	1.06	3.6	4.05	5.16	4.02	3.8	5.23	7.08	8.1	11.54	6.05





## **Future Prospect**



# **Industry Outlook**



- The CAGR of the Semi. market size over the next 10 years is approximately 8%
- In 2025, the global Semi. market is expected to grow 11%
- Driven by demand in artificial intelligence (AI) and high-performance computing applications, the semi. market in Taiwan:

Year	Market Size (Billion US\$)	Groth Rate (%)				
2024	165	22				
2025	187	16.5				

- 12-inch semi. advanced process continues to thrive
  - Front-End: 5nm \ 3nm \ 2nm \ 1nm
  - Advanced Package: Fan-Out \ 2.5D (CoWoS) \ 3D (SoIC)...etc

# Industry Outlook – 2



- The CAGR of Advanced Packaging over the next 10 years is estimated at 11%.
- CoWoS production capacity is expanding substantially to meet the rising demand for AI chip applications
- Al applications are expanding from data centers to personal devices
- China's semi. production capacity is expanding, intensifying price competition in mature process nodes.
- Compound Semi:
  - Investment in electric vehicles (EVs) and 5G is stagnant
  - Mobile phone demand is saturating, leading to reduced GaAs production
  - SiC demand gradually increases, with new capacities slowing down
  - Micro LED adoption is progressing slowly

# **Expansion Plan**



- 2023: New equipment production base in Hukou (off-site)
- 2024: Expansion of existing equipment production at Hukou Plant
- 2025 Q4: Wafer Reclaim Capacity 50K/Month at Hukou Plant
- 2026 Q4: 9,000 M-Square new building (Plant II) for equipment manufacturing, and warehouse next to Hukou Plant (Plant I)
- 2026 Q4: Wafer Reclaim Capacity 30K/Month at Hukou Plant
- 2027 Q1: 20,000 M-Square new building for equipment manufacturing, and warehouse in Tainan (southern Taiwan)
- 2027: Wafer Reclaim Capacity 50K/Month at Hukou Plant



## Thank You!

https://www.scientech.com.tw